

FIG. 1

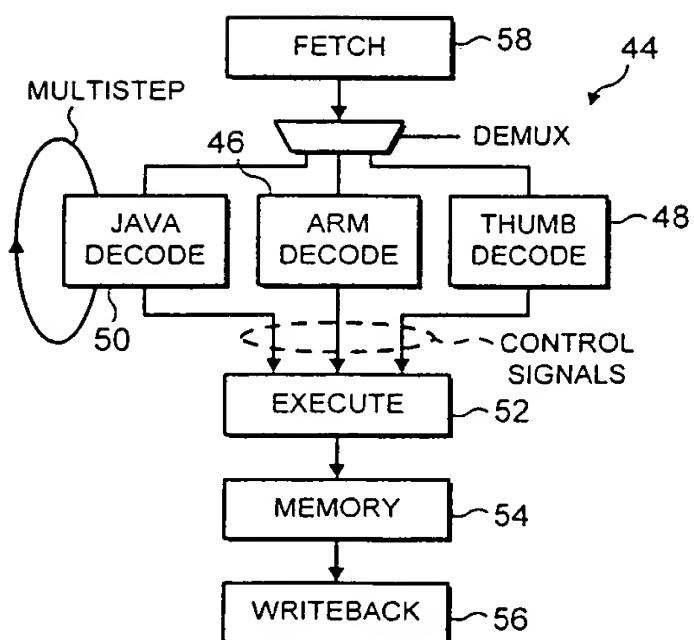


FIG. 2

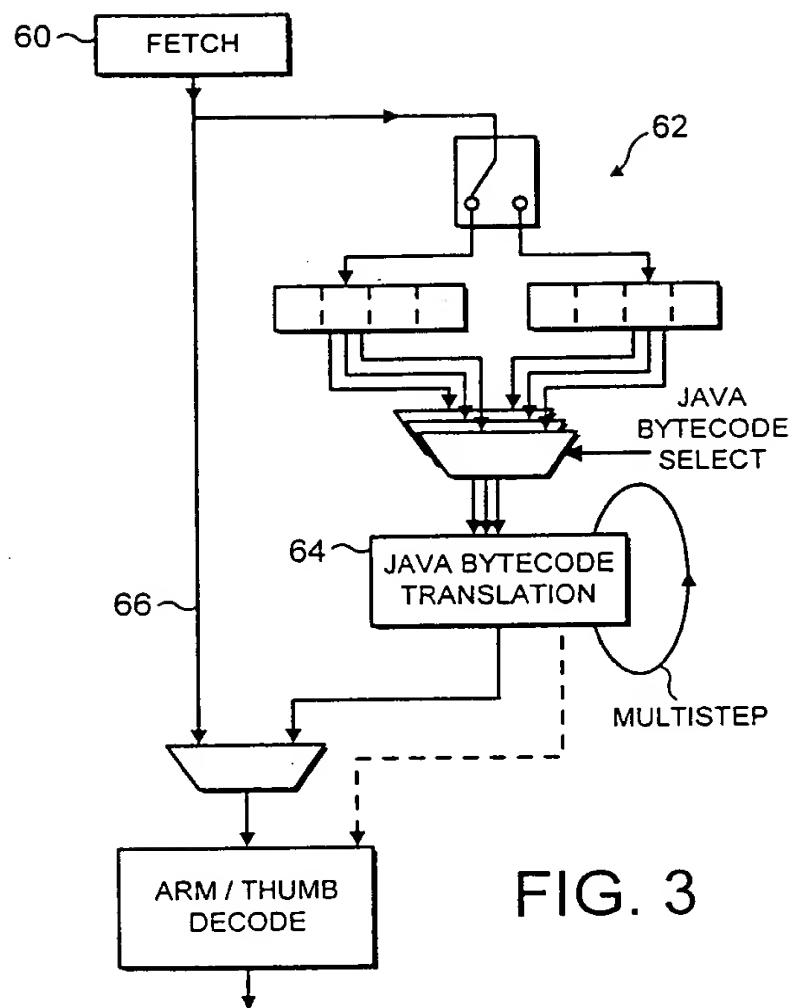


FIG. 3

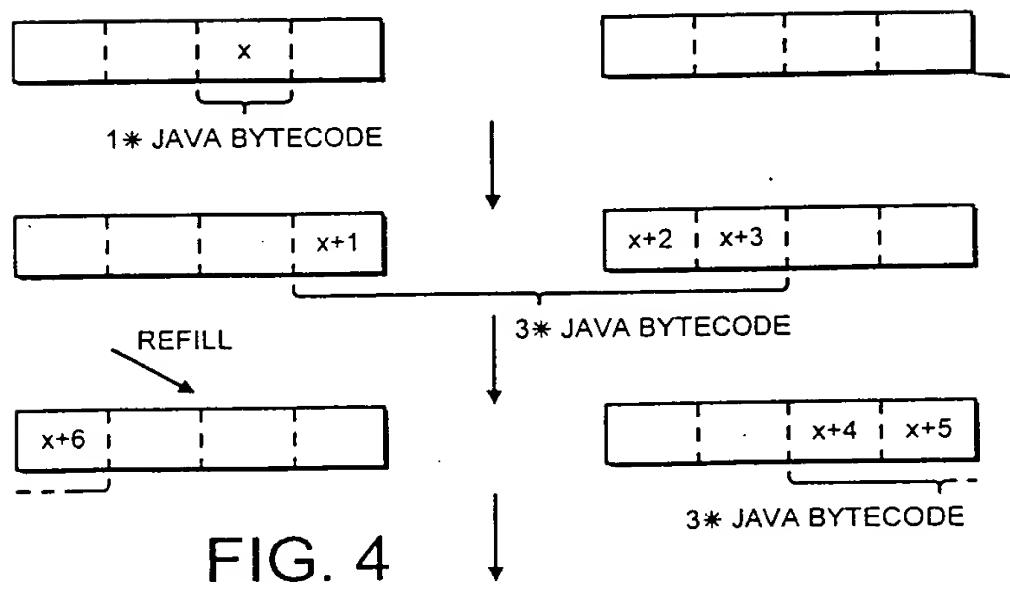
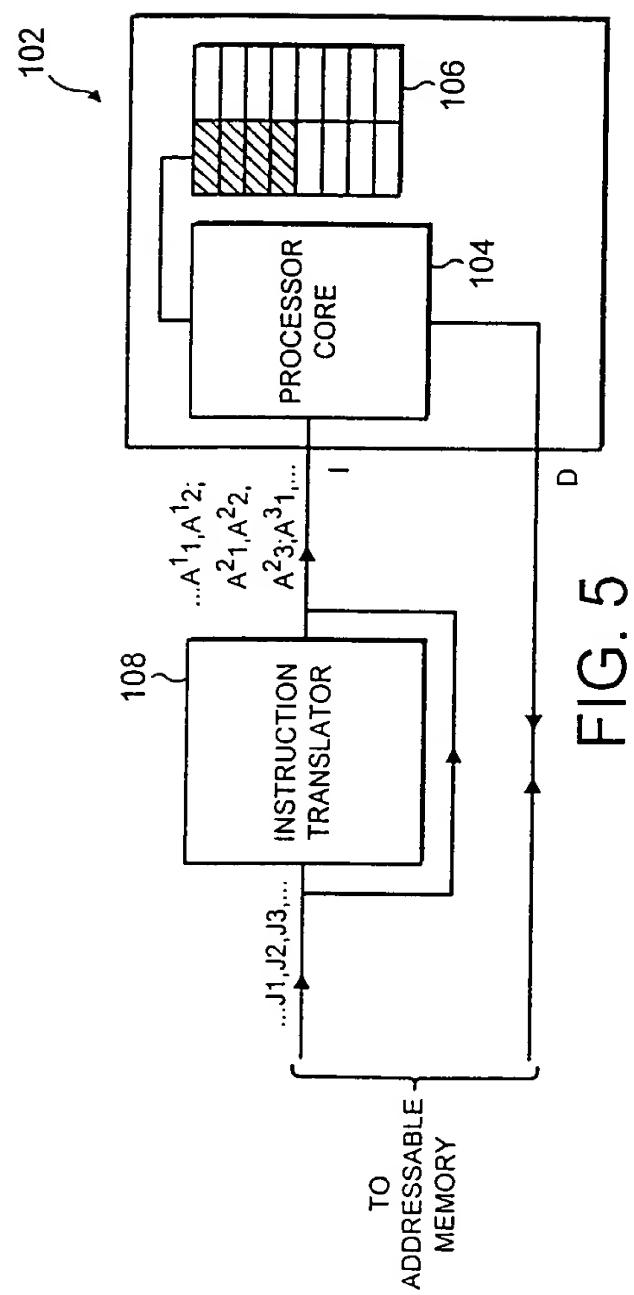


FIG. 4



ARM INSTRUCTION(S)	STATE	ARM INSTRUCTION(S)	STATE
iadd (RF=2,RF>Ø)	00000	LDR RØ[RStack,#-4] (POP)	01000
		LDR R3[RStack,#-4] (POP)	00111
		ADD R3,R3,RØ	
RØ	E	SOA TOS	SOA TOS
R1	E	E	E
R2	E	E	E
R3	E	E	SOB TOS-1
		(SOA+SOB) TOS	
Java Instruction		Java Instruction	
		Load ¹ (RF=Ø,RE>2)	Load ² (RF=Ø,RE>2)
		LDR R1,[Rvars,#4] LDR RØ,[Rvars,#0]	STR R3[RStack],#-4 (PUSH)
			LDR R3,[Rvars,#4] LDR R2,[Rvars,#0]
ARM INSTRUCTIONS			
STATE	00000	00100	01000
			10011
RØ	E	SOC TOS-1	SOC TOS-1
R1	E	SOD TOS	SOD TOS
R2	E	E	E
R3	(SOA+SOB) TOS	(SOA+SOB) TOS	E
			SOF TOS

FIG. 6

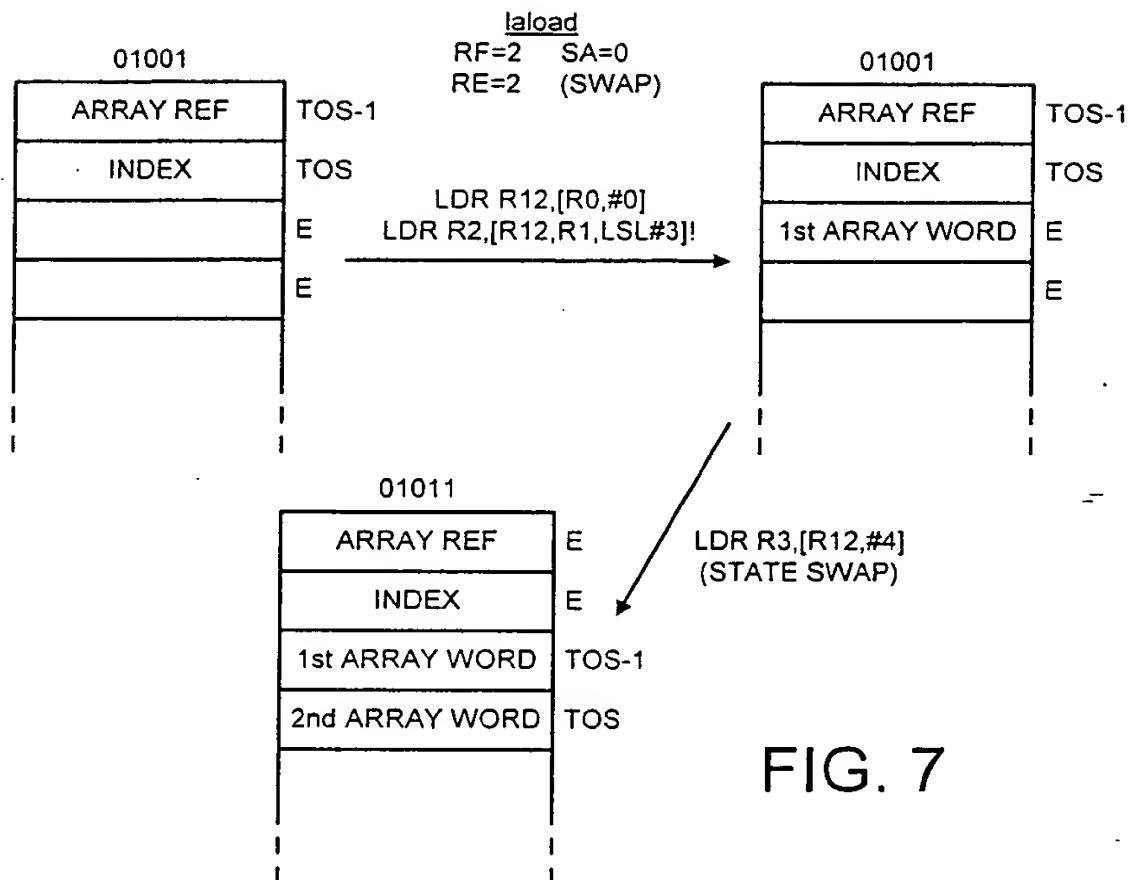


FIG. 7

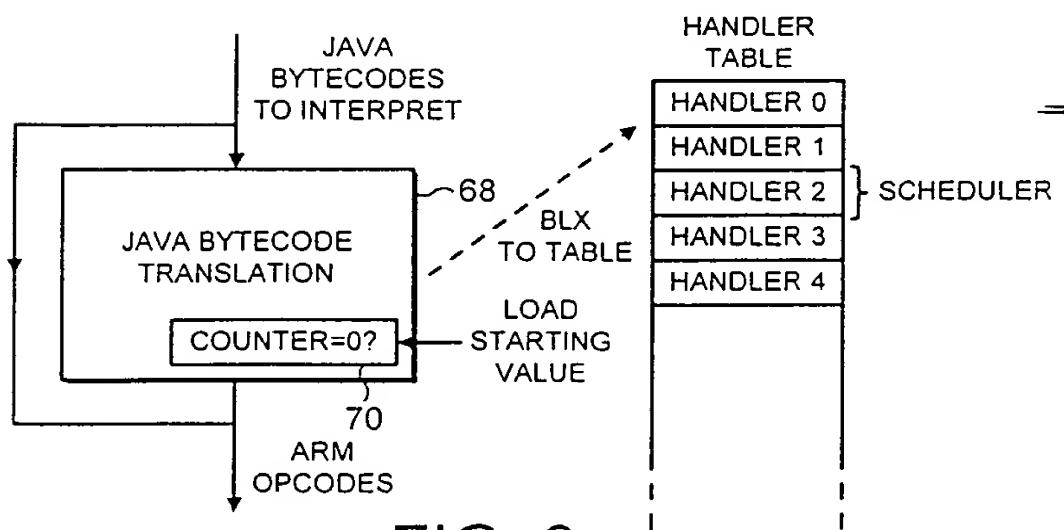


FIG. 9

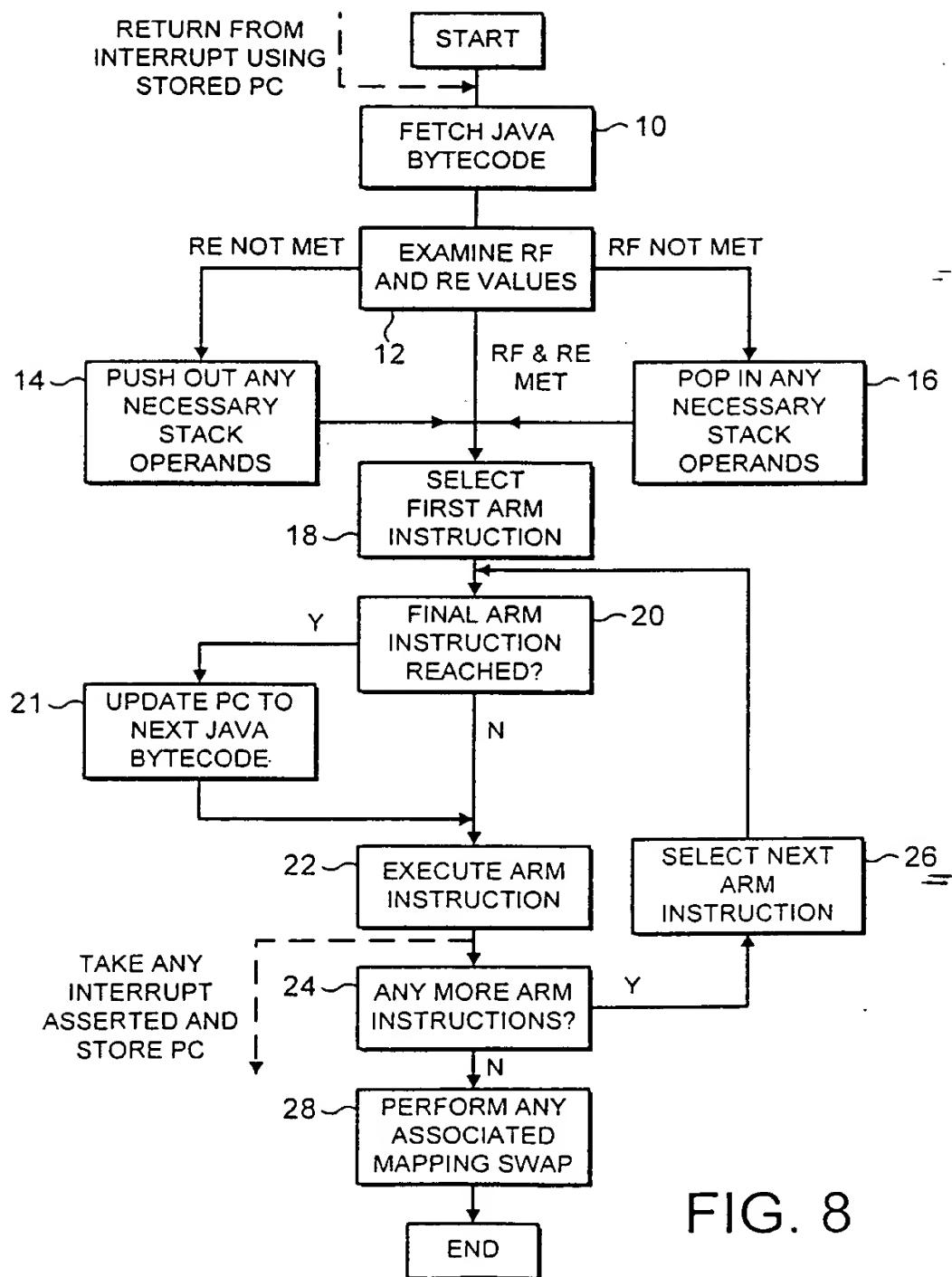
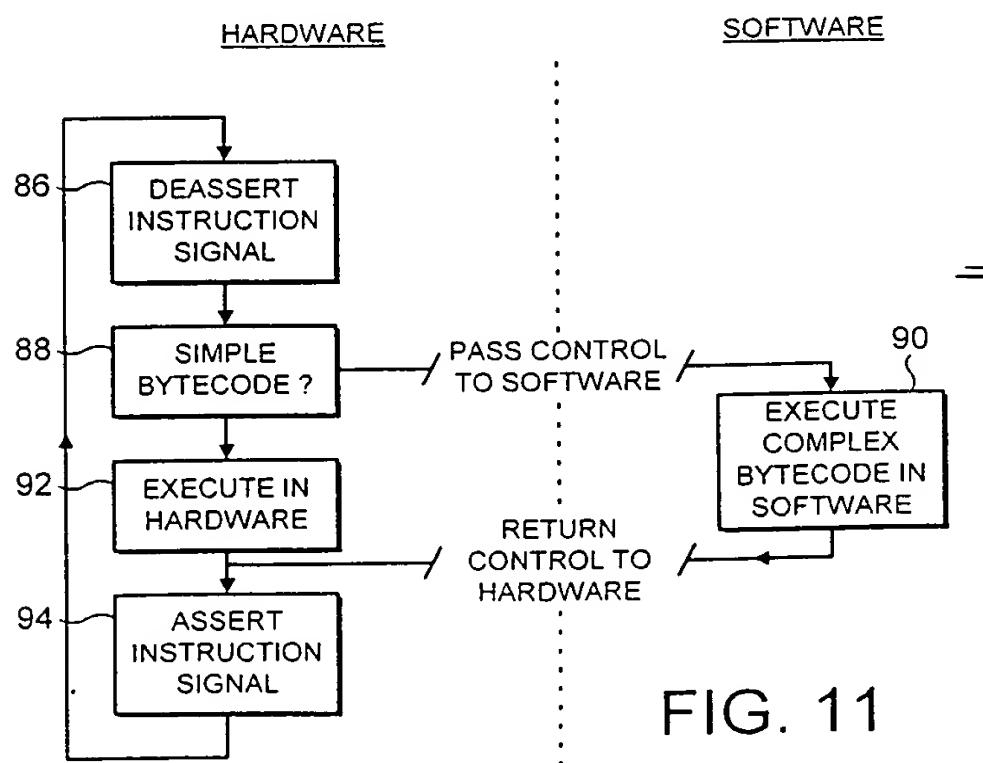
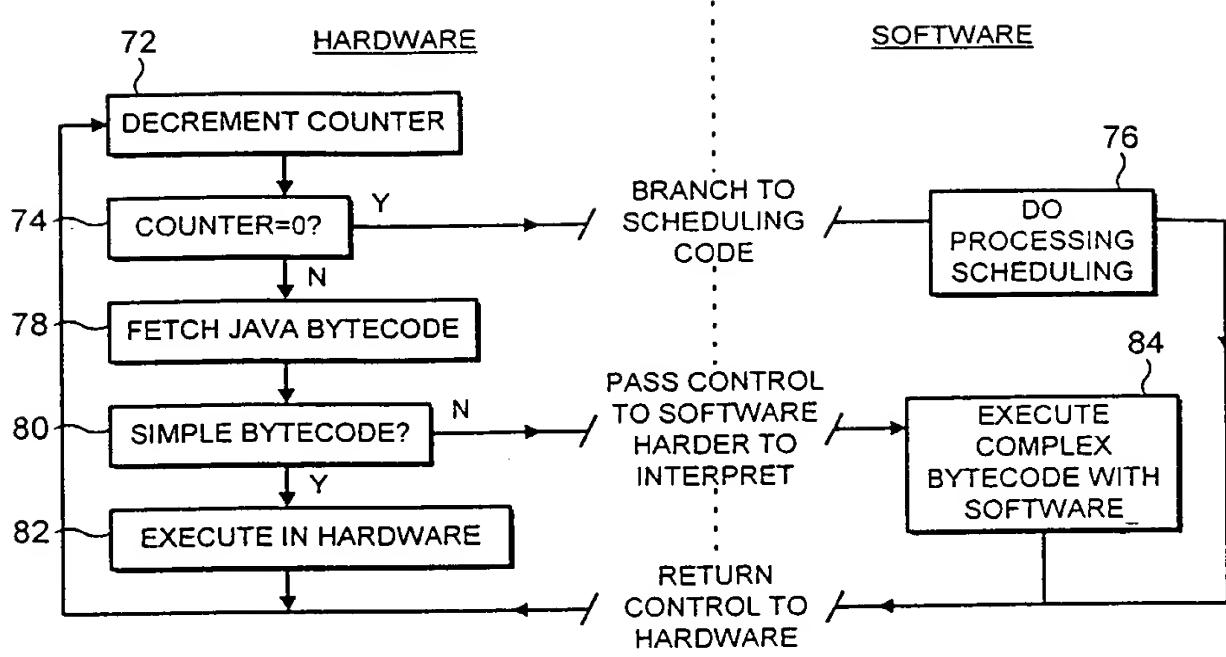


FIG. 8



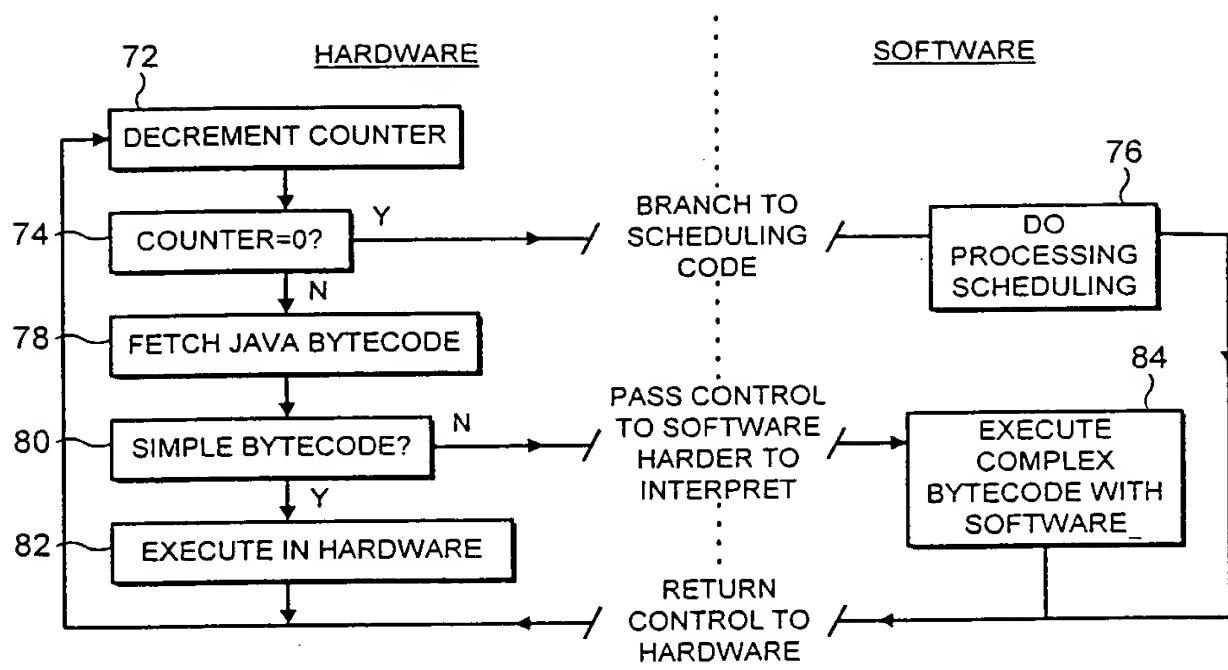


FIG. 10

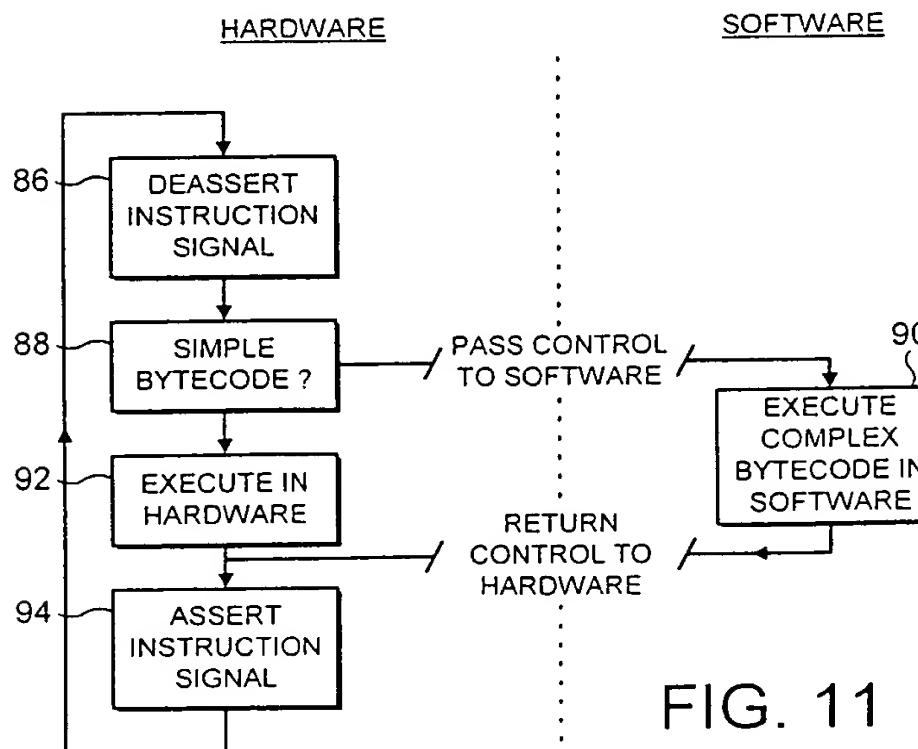


FIG. 11

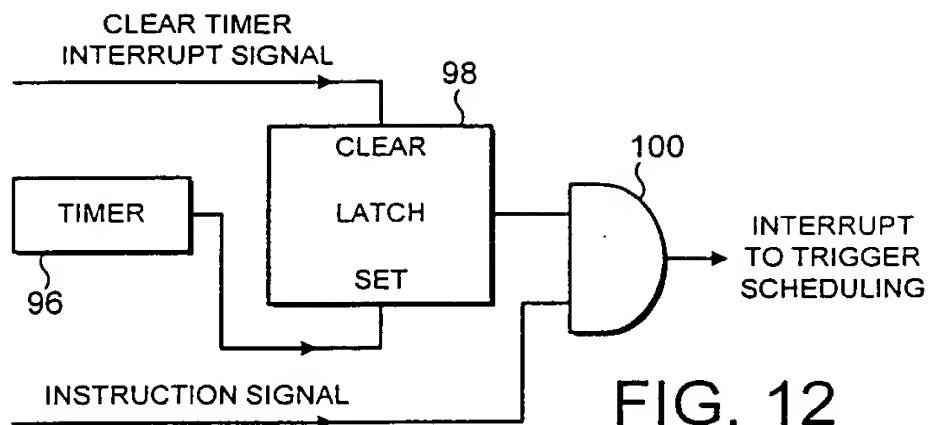


FIG. 12

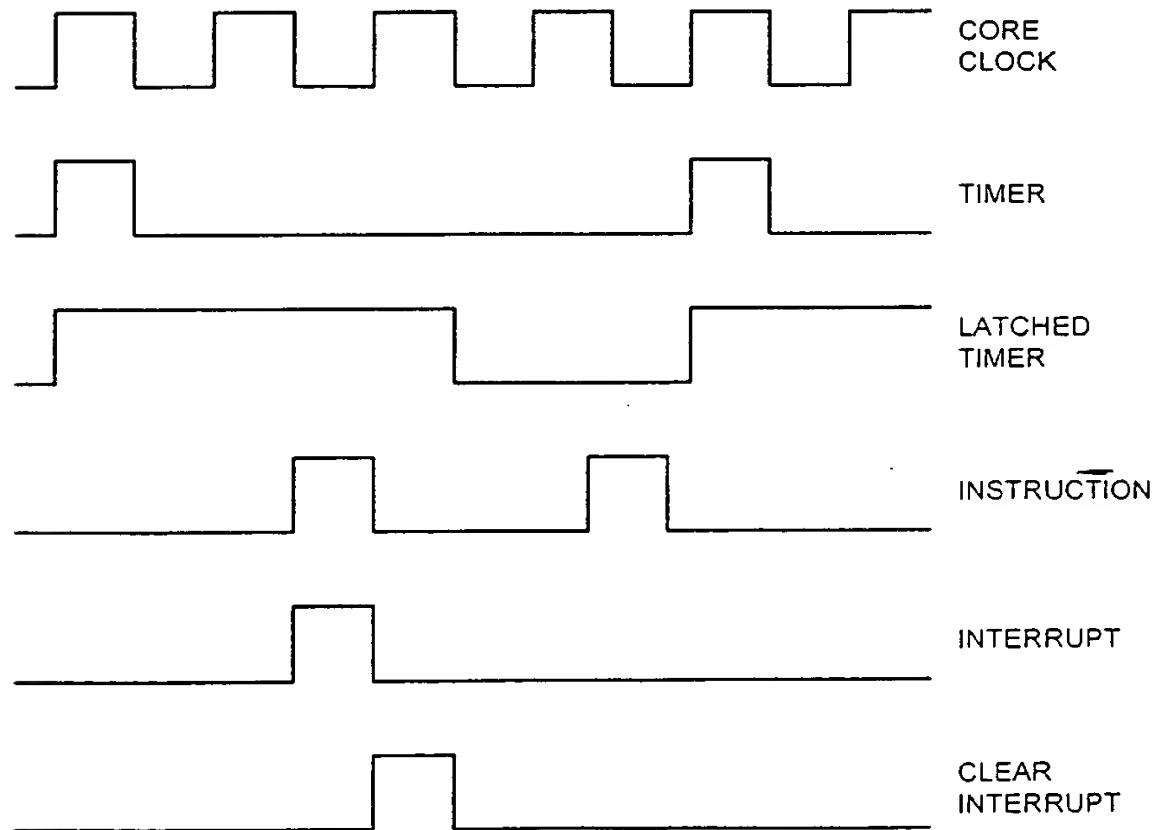


FIG. 13

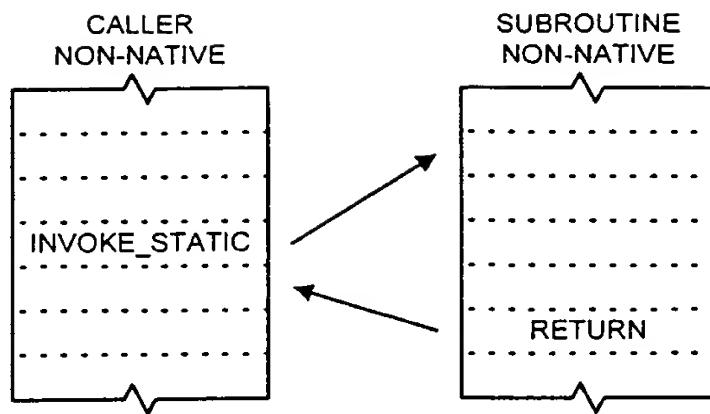


FIG. 14
PRIOR ART

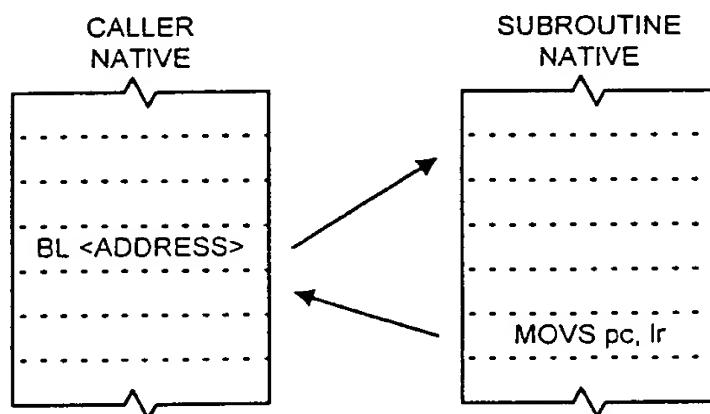


FIG. 15
PRIOR ART

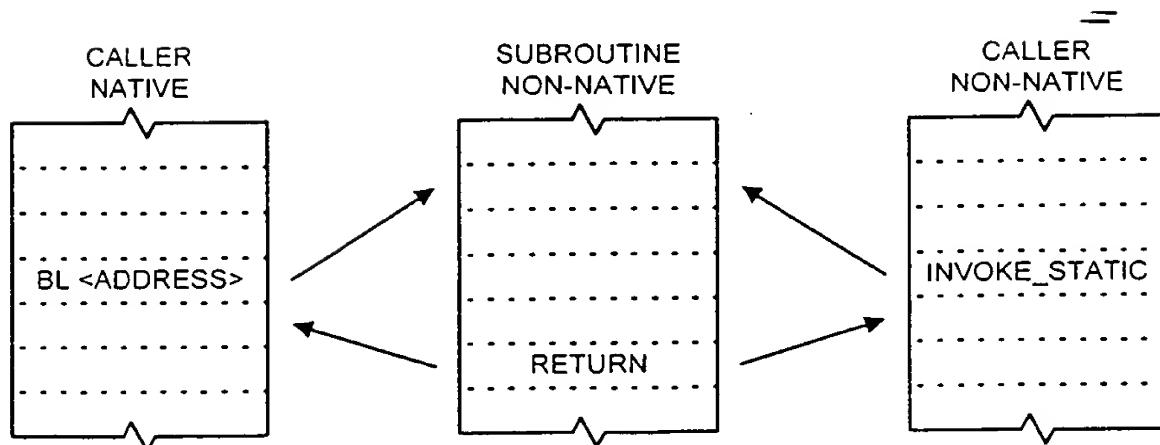


FIG. 16
PRIOR ART

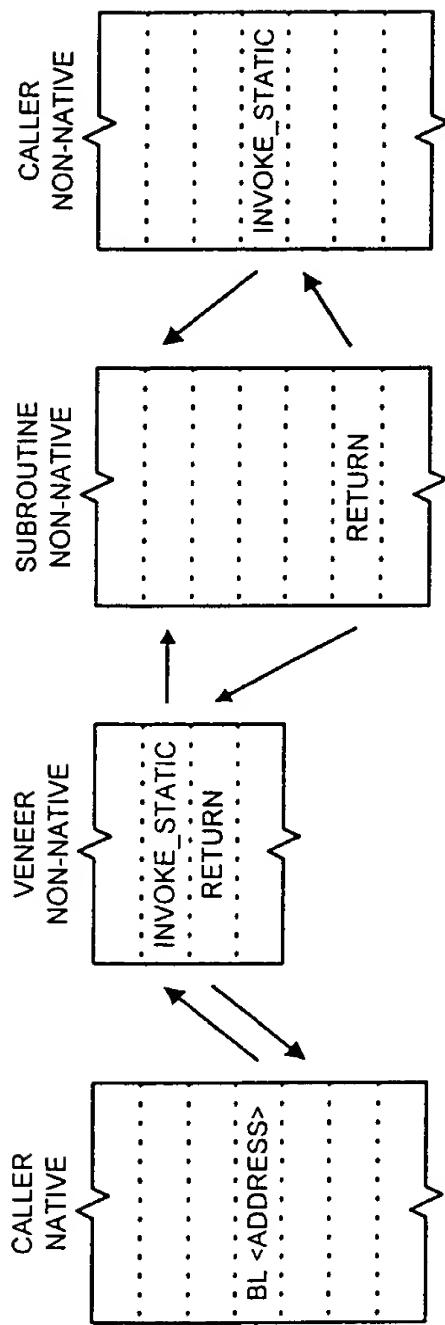


FIG. 17
PRIOR ART

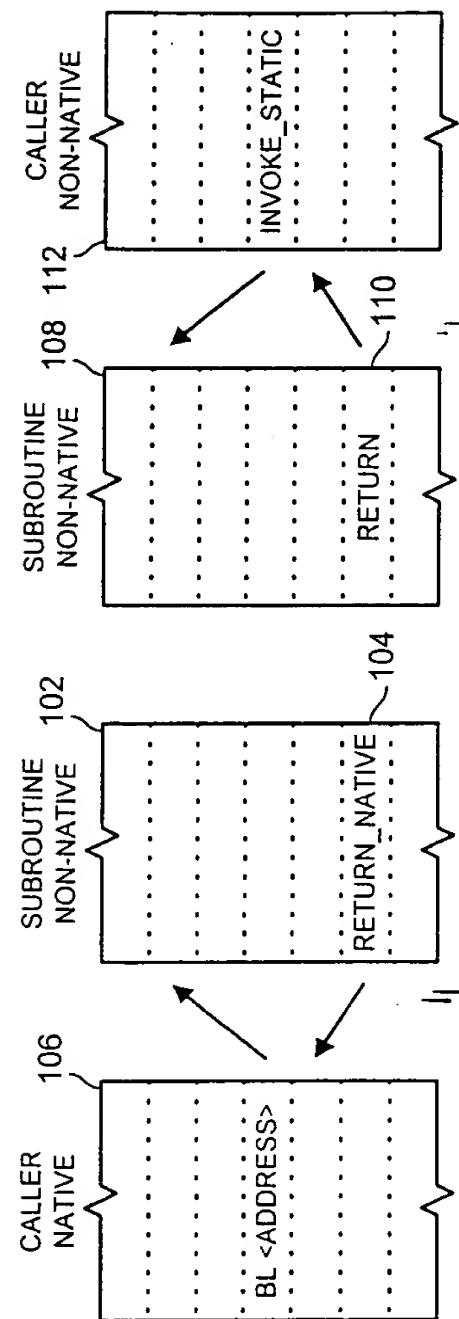


FIG. 18

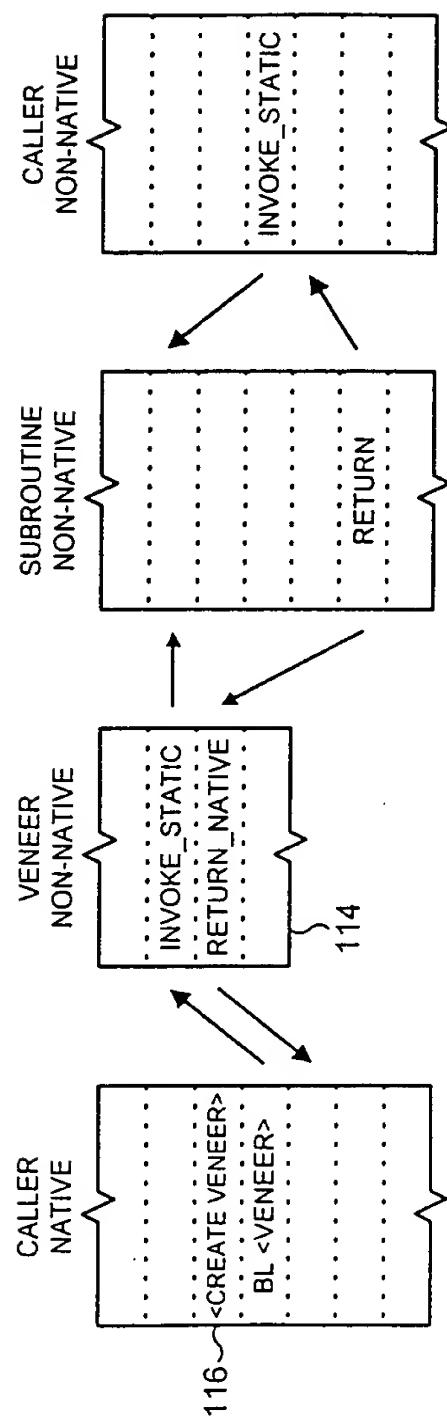


FIG. 19

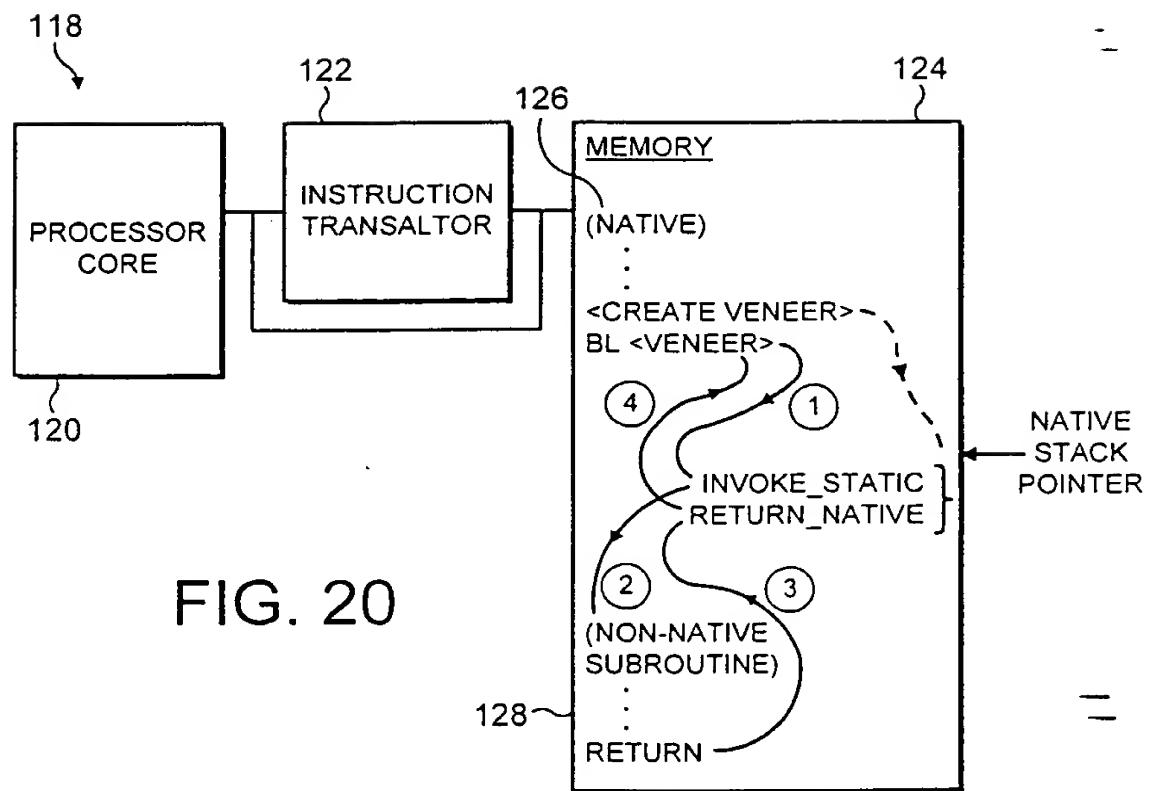


FIG. 20

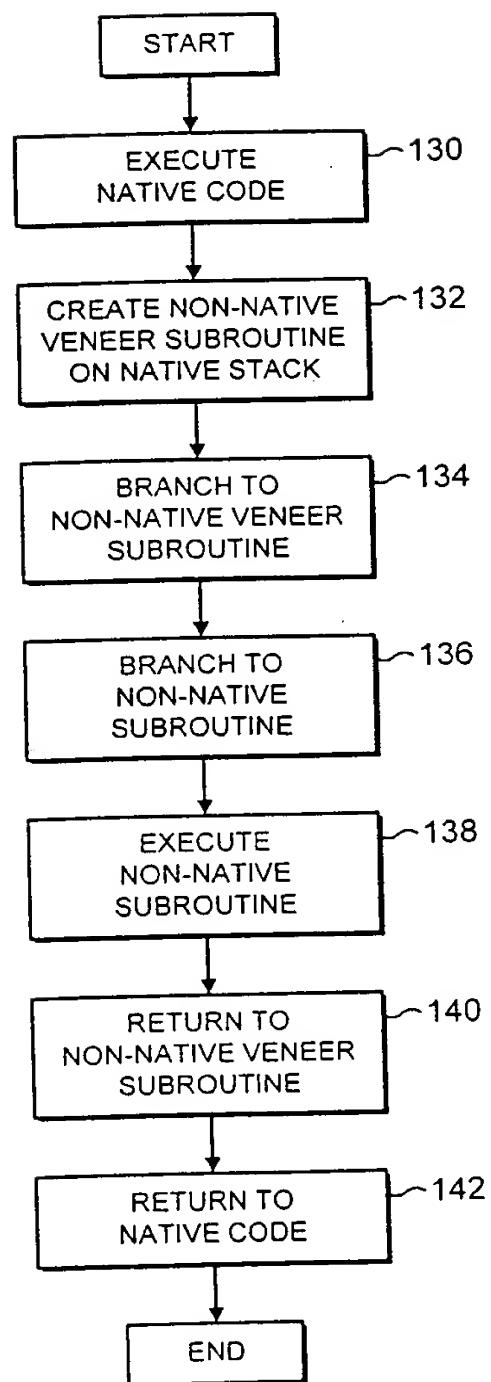


FIG. 21